



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2841

Examiner: Ishwarbhai B Patel

In Re PATENT APPLICATION Of:

Applicants : Norio TAKAHASHI

Appln. No. : 10/775,232

Filed : February 11, 2004

For : MODULE CIRCUIT BOARD FOR  
SEMICONDUCTOR DEVICE  
HAVING BARRIERS TO ISOLATE  
I/O TERMINALS FROM SOLDER

Attorney Ref.: F97ED0740-DIV3

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) AMENDMENT  
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November 22, 2006

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Va 22313-1450

Sir:

In response to the Examiner's Office Action mailed November  
August 22, 2006, please amend the above-identified application as follows: